

AMENDMENTS

Please amend the present application as follows:

Claims

The following is a copy of Applicants' claims that identifies language being added with underlining ("____") and language being deleted with strikethrough ("——"), as is applicable:

1. (Previously Presented) A system comprising:

a threshold detector circuit;

a first switching circuit for enabling access to a back-up power source, the first switching circuit comprising at least a first transistor; and

an inverting switch coupled between the first switching circuit and the threshold detector circuit, the inverting switch comprising an input and an output, the inverting switch configured to receive a signal at the input, invert the signal, and provide the inverted signal from the output to the first switching circuit, the inverting switch further comprising a delay circuit, the delay circuit configured to provide a substantially-immediate high-output during an off-to-on transition at the output of the inverting switch and a switching delay during an on-to-off transition at the output of the inverting switch;

wherein the threshold detector circuit is configured to cause the first switching circuit to enable access to the back-up power source responsive to a voltage provided by a primary power source dropping below a predetermined threshold.

2. (Previously Presented) The system of claim 1, further comprising:

a second switching circuit for enabling access to the primary power source, the second switching circuit comprising at least one transistor.

3. (Previously Presented) The system of claim 2, wherein the threshold detector circuit is configured to cause the second switching circuit to enable access to the primary power source responsive to a voltage provided by the primary power source rising above the predetermined threshold.
4. (Canceled)
5. (Previously Presented) The system of claim 1, wherein the inverting switch comprises at least one transistor.
6. (Previously Presented) The system of claim 1, further comprising:
an inverter coupled between the inverting switch and the threshold detector circuit.
7. (Original) The system of claim 6, wherein the inverter comprises a comparator.
8. (Original) The system of claim 1, wherein the first switching circuit comprises a second transistor coupled to the first transistor.
9. (Original) The system of claim 8, wherein an emitter of the first transistor is coupled to a collector of the second transistor.
10. (Previously Presented) The system of claim 9, wherein current flow between the first and second transistors terminates access to the back-up power source.

11. (Previously Presented) The system of claim 9, wherein resistance to current flow between the first and second transistors enables access to the back-up power source.

12-17. (Canceled)

18. (Currently amended) A system comprising:

a threshold detector circuit;

a first switching circuit for enabling access to a back-up power source, the first switching circuit comprising at least a first transistor;

a second switching circuit for enabling access to a primary power source, the second switching circuit comprising at least one transistor;

an inverting switch coupled between the first switching circuit and the threshold detector, the inverting switch configured to provide a substantially instantaneous high output during an on-to-off transition at an input of the inverting switch and a delayed-off output during an off-to-on transition at the input of the inverting switch, the delayed-off output of a longer duration that substantially instantaneous; and

an inverter coupled between the inverting switch and the threshold detector circuit;

wherein the threshold detector circuit is configured to cause the first switching circuit to enable access to the back-up power source responsive to a voltage provided by the primary power source dropping below a predetermined threshold; and

wherein the threshold detector circuit is configured to cause the second switching circuit to enable access to the primary power source responsive to a voltage provided by the primary power source rising above the predetermined threshold.

19-20. (Canceled)

21. (Previously Presented) A system comprising:
- a threshold detector circuit;
 - a first switching circuit for enabling access to a back-up power source, the first switching circuit comprising at least a first transistor; and
 - an inverting switch coupled between the first switching circuit and the threshold detector circuit;
- wherein the threshold detector circuit is configured to cause the first switching circuit to enable access to the back-up power source responsive to a voltage provided by a primary power source dropping below a predetermined threshold;
- wherein the threshold detection circuit further comprises:
- a comparator having a first input connection for receiving a first input voltage, a second input connection for receiving a second input voltage, and an output connection for providing an output voltage;
 - a first branch including a first resistor and a first capacitor that are coupled in series between the first input connection and the output connection;
 - and
 - a second resistor that is coupled between the first input connection and the output connection, and that is coupled in parallel to the first branch.

22. (Canceled)
23. (Previously Presented) The system of claim 18, wherein the inverting switch comprises at least one transistor.
24. (Previously Presented) The system of claim 18, wherein the inverter comprises a comparator.

25. (Previously Presented) The system of claim 18, wherein the first switching circuit comprises a second transistor coupled to the first transistor, an emitter of the first transistor coupled to a collector of the second transistor.

26. (Previously Presented) The system of claim 25, wherein current flow between the first and second transistors terminates access to the back-up power source, and resistance to current flow between the first and second transistors enables access to the back-up power source.

27. (Previously Presented) The system of claim 21, further comprising:
a second switching circuit for enabling access to the primary power source, the second switching circuit comprising at least one transistor, wherein the threshold detector circuit is configured to cause the second switching circuit to enable access to the primary power source responsive to a voltage provided by the primary power source rising above the predetermined threshold.

28. (Previously Presented) The system of claim 21, further comprising:
an inverter coupled between the inverting switch and the threshold detector circuit.

29. (Previously Presented) The system of claim 21, wherein the first switching circuit comprises a second transistor coupled to the first transistor.

30. (Previously Presented) The system of claim 29, wherein current flow between the first and second transistors terminates access to the back-up power source,

and wherein resistance to current flow between the first and second transistors enables access to the back-up power source.